

# 1<sup>ST</sup> ACTIVATION DAY

*Activate your semiconductor advantage!*

## 9:00 - 10:30 Entry & Activation Layer

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### 10:30 - 11:05 Opening & Framing

- **Welcome & Objectives of the Day** | Emm. Zervakis, HCCC President
  - **What is HCCC – Practical Perspective** | John Kikidis, HCCC Vice President
  - **Chips Initiatives in Switzerland and the Innovation Hub Semiconductor Metrology & Inspection** | Robert Rudolph, CEO, Park Innovaare
  - **Flow of the Day – From Technology to Collaboration** | Konst. Tsiapali (Project Coordinator)
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### 11:05 - 11:45 Access & Onboarding for Design Tools, Pilot Lines & HPC Tools, infrastructures, demos & real use cases

- **Scope – What HCCC offers** | George Konstantinidis, Director of Research, FORTH
  - **Pilot Lines – Use & Industrial Example**
    - **Access to European & Greek Pilot Lines - Technology Nodes & PDKs** | Manos Trichas, Innovation & Technology Manager SMARTEC, FORTH
    - **Exploiting a Greek pilot line for enabling compact active antennas** | Kostis Vryssas, VP Engineering - RF/mm-Wave IC Design, ArgoSemi
  - **HPC Infrastructures & Use Cases**
    - **"UNIWA AI Innovation Hub"** | Christoforos Kachris, Asst. Professor
    - **DUTH HPC Services - CPU & GPU Infrastructure** | Aggelos Iosif Fyrigos, Professor, Post Doctoral Researcher, DUTH
    - **ARISTOTELIS: High-Performance Computing Cluster @ AUTH** | Alkis Chatzopoulos, Professor, AUTH
  - **Access to EDA Tools** | George Stamoulis, Professor, UTH
  - **ServicePlace – Your Entry Point to HCCC Services** | George Konstantinidis, Director of Research, FORTH
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### 11:45 - 12:30 Networking / Matchmaking & Coffee Break

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### 12:30 - 13:15 Services & Technology Transfer

- **What HCCC services provide** | Grigoris Zisis, Project Manager, INN - NCSR "Demokritos"
- **Real examples & use cases**
  - **Fabrication of Photonic Integrated Circuits and Sensors** | Eleni Makarona, Director of Research, INN - NCSR "Demokritos"
  - **Design Services & Soft Semi Services from University of Patras** | Alexios Birbas, Professor, ECE, University of Patras
  - **Design and development of intelligent embedded systems** | Dimosthenis Ioannidis, Senior Researcher, ITI, CERTH
- **From use case to service delivery** | Grigoris Zisis, Project Manager, INN – NCSR "Demokritos"

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**13:15 - 15:15**    **Lunch & Networking / Matchmaking**

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**14:45 - 15:15**    **WP5 – Skills Validation & Training Activation Day**

- **Skills Gaps & Industrial Needs** | George Panagopoulos, Asst. Professor NTUA
  - **Indicative Advanced Training Areas & Specialization Paths** | George Zervakis, Asst. Professor NTUA
  - **Indicative Training Paths & Real Examples**
    - **From idea to project development** | Paris Chrysos, Asst. Professor NKUA
    - **Structured Training Path: From low power microelectronics to Energy Autonomous embedded systems** | Gregory Doumenis, Asst. Professor Uoi
  - **HCCC Training Ecosystem & Next Steps** | George Panagopoulos, Asst. Professor NTUA
  - **Transition to Lounge** | George Panagopoulos, Asst. Professor NTUA
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**15:15 - 16:00**    **Networking / Matchmaking & Coffee Break**

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**16:00 - 16:45**    **Business, Funding & Innovation Ecosystem**

From services to collaborations, funding and innovation opportunities

- **Opening Remarks** | Sotiris Bantas (HCCC WP Leader)
  - **Fireside Chat: Investors & Startups** | Sotiris Bantas & invited investors  
Discussion on:
    - Reverse pitching from deep-tech investors
    - What semiconductor and advanced technology companies should present to investors
    - Technology innovation, scaling and collaboration pathways
  - **Next Steps & Transition to Matchmaking** | Sotiris Bantas (HCCC WP Leader)
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**16:45 - 18:00**    **Final Activation – 1:1 Meetings & Conversion**

- Targeted 1:1 meetings
- WP clinics (WP3 / WP4 / WP5 / WP6)
- ServicePlace onboarding & EOI submission
- Final matchmaking & follow-up discussions

